

< IGBT MODULES >

CM150DX-24S

HIGH POWER SWITCHING USE INSULATED TYPE

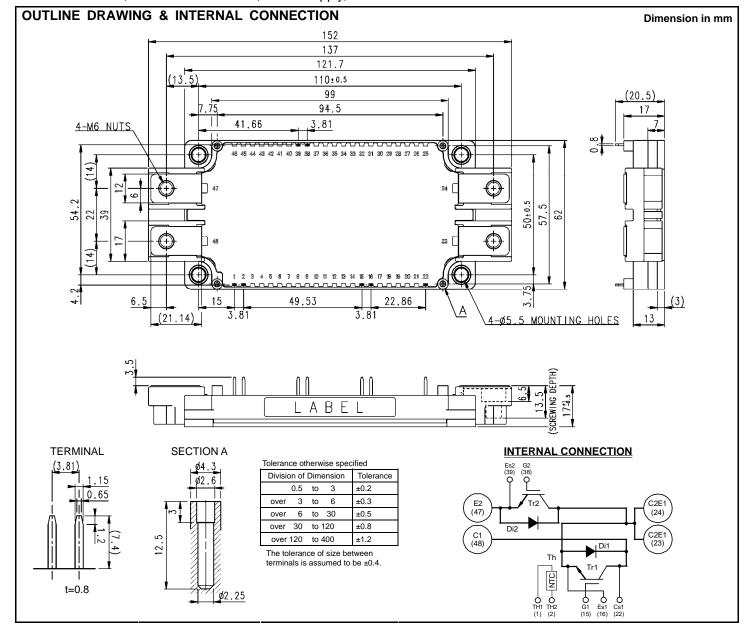


Dual (Half-Bridge)

- Flat base Type
- Copper base plate (non-plating)
- •Tin plating pin terminals
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



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ABSOLUTE MAXIMUM RATINGS (T_{j} =25 °C, unless otherwise specified) INVERTER PART IGBT/FWDi

Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V	
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Collector current	DC, T _C =120 °C (Note2, 4)	150	Α	
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	300	A	
P _{tot}	Total power dissipation T _C =25 °C (Note2, 4)		1150	W	
I _E (Note1)	Emitter current	(Note2)	150	۸	
I _{ERM} (Note1)	Emilier current	Pulse, Repetitive (Note3)	300	А	

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T _{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note4)	125	
Tjop	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_j =25 °C, unless otherwise specified) INVERTER PART IGBT/FWDi

Symbol	Item	Conditions			Limits		Unit
Symbol	item	Conditions	Containent		Тур.	Max.	Offic
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited	V _{GE} =V _{GES} , C-E short-circuited		-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =15 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =150 A (Note5),	T _j =25 °C	-	1.80	2.25	
		V _{GE} =15 V,	T _j =125 °C	-	2.00	-	V
V_{CEsat}	Collector-emitter saturation voltage	(Terminal)	T _j =150 °C	-	2.05	-	
V CEsat	Collector-entitler saturation voltage	I _C =150 A (Note5),	T _j =25 °C	-	1.70	2.15	
		V _{GE} =15 V,	T _j =125 °C	-	1.90	-	V
		(Chip)	T _j =150 °C	-	1.95	-	
Cies	Input capacitance			-	-	15	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	3.0	nF
C _{res}	Reverse transfer capacitance		-	-	0.25		
Q _G	Gate charge	V_{CC} =600 V, I_{C} =150 A, V_{GE} =15 V	/	-	350	-	nC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =150 A, V _{GE} =±15 V,		-	-	800	- ns
t _r	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time			-	-	600	
t _f	Fall time	$R_G=0 \Omega$, Inductive load		-	-	300	
		I _E =150 A (Note5),	T _j =25 °C	-	1.80	2.25	V
		G-E short-circuited,	T _j =125 °C	-	1.80	-	
V _{EC} (Note1)	Conitto y collector voltage	(Terminal)	T _j =150 °C	-	1.80	-	
V _{EC} `	Emitter-collector voltage	I _E =150 A (Note5),	T _j =25 °C	-	1.70	2.15	
		G-E short-circuited,	T _j =125 °C	-	1.70	-	V
		(Chip)	T _j =150 °C	-	1.70	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =150 A, V _{GE} =±15	V,	-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	$R_G=0 \Omega$, Inductive load		-	8.0	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =150 A,		-	24.2	-	m l
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=0 \Omega, T_{j}=150 \text{ °C},$		-	16.0	-	- mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	12.2	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	-	1.8	mΩ
r _g	Internal gate resistance	Per switch		-	13	-	Ω

< IGBT MODULES > CM150DX-24S HIGH POWER SWITCHING USE INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont; $T_j=25$ °C, unless otherwise specified) NTC THERMISTOR PART

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Symbol	Item	Conditions	Limits			Unit		
	item	Conditions	Min.	Min. Typ. Max.	Offic			
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ		
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%		
B _(25/50)	B-constant	Approximate by equation (Note7)	-	3375	-	K		
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW		

THERMAL RESISTANCE CHARACTERISTICS

	LOID TAILOE CHARACTERIO HOO					
Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Тур.	Max.	Oill
$R_{th(j-c)Q}$	Thermal resistance (Note4)	Junction to case, per Inverter IGBT	-	-	0.13	K/W
$R_{th(j-c)D}$		Junction to case, per Inverter FWDi	-	-	0.23	
R _{th(c-s)}	Contact thermal resistance (Note4)	Case to heat sink, per 1 module,	-	15	-	K/kW
		Thermal grease applied (Note7)				N/KVV

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Conditions		Limits		
		Conditions			Тур.	Max.	Unit
M _t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m
d	Creepage distance	Terminal to terminal		11.55	-	-	- mm
d _s		Terminal to base plate		12.32	-	-	
	Clearance	Terminal to terminal		10.00	-	-	mm
d _a		Terminal to base plate		10.85	-	-	mm
m	Weight	-		-	350	-	g
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm

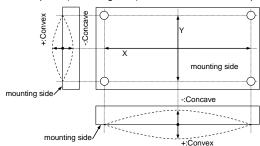
- Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).
 - 2. Junction temperature (T_j) should not increase beyond T_{jmax} rating.
 - 3. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed T_{jmax} rating.
 - 4. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 R_{25} : resistance at absolute temperature T_{25} [K]; T_{25} =25 [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



- 9. Use the following screws when mounting the printed circuit board (PCB) on the stand offs.
 - "ST2.6×10 or ST2.6×12 self tapping screw"

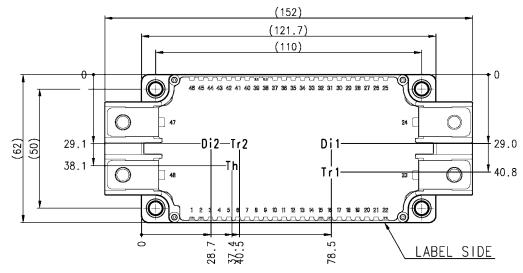
The length of the screw depends on the thickness of the PCB.

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
	nem	Conditions	Min.	Тур.	Max.	Offic
Vcc	(DC) Supply voltage	Applied across P-N terminals	=	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	0	-	30	Ω

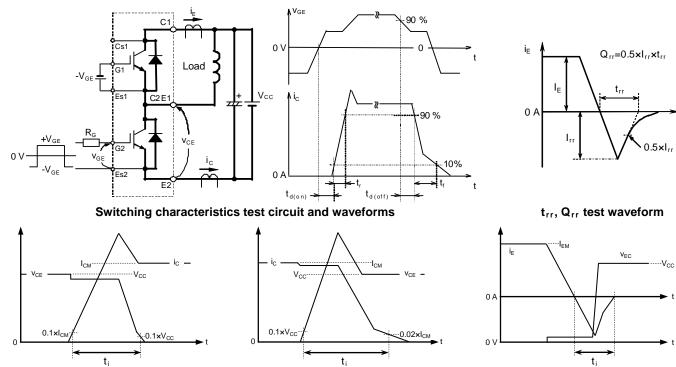
CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm



Tr1/Tr2: IGBT, Di1/Di2: FWDi, Th: NTC thermistor

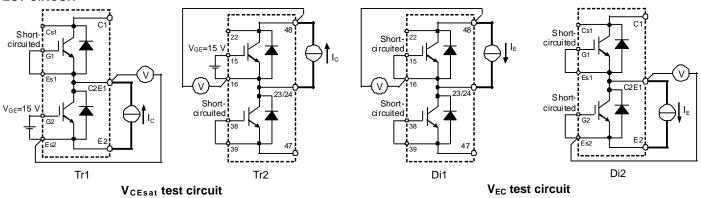
TEST CIRCUIT AND WAVEFORMS



IGBT Turn-off switching energy Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT

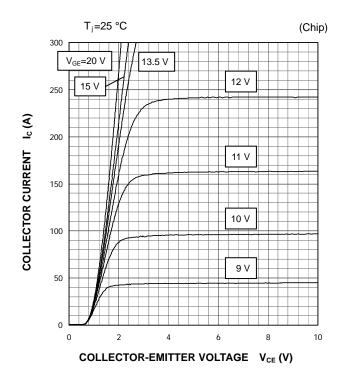
IGBT Turn-on switching energy



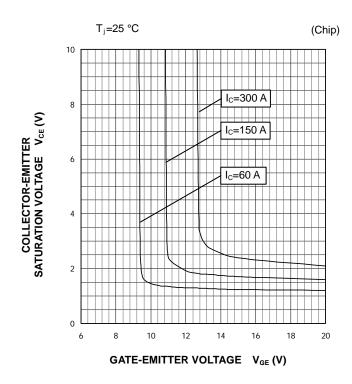
FWDi Reverse recovery energy

PERFORMANCE CURVES INVERTER PART

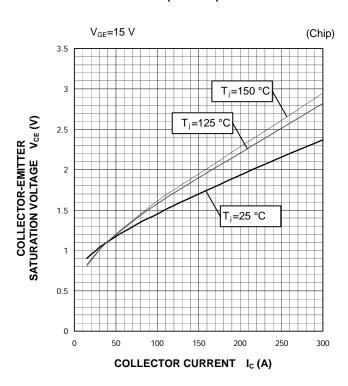
OUTPUT CHARACTERISTICS (TYPICAL)



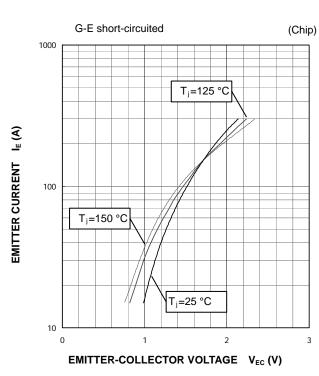
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



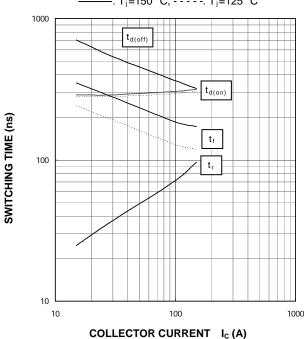
PERFORMANCE CURVES

INVERTER PART

HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

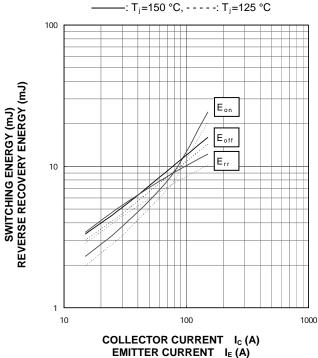
 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =0 Ω , INDUCTIVE LOAD

....: T_j =150 °C, ----: T_j =125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

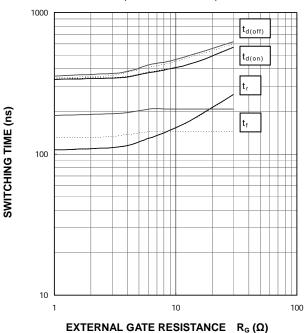
(TYPICAL)
V_{CC}=600 V, V_{GE}=±15 V, R_G=0 Ω,
INDUCTIVE LOAD, PER PULSE



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

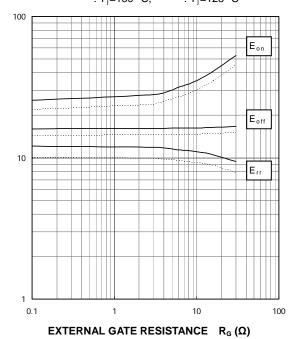
V_{CC}=600 V, V_{GE}=±15 V, I_C=150 A, INDUCTIVE LOAD

.....: T_j=150 °C, - - - - : T_j=125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

(TYPICAL) V_{CC} =600 V, V_{GE} =±15 V, I_C / I_E =150 A, INDUCTIVE LOAD, PER PULSE ——: T_i =150 °C, - - - - -: T_i =125 °C

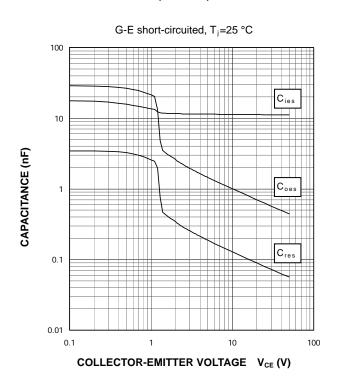


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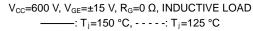
SWITCHING ENERGY (mJ)
REVERSE RECOVERY ENERGY (mJ)

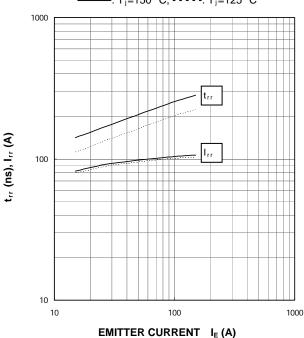
PERFORMANCE CURVES INVERTER PART

CAPACITANCE CHARACTERISTICS (TYPICAL)

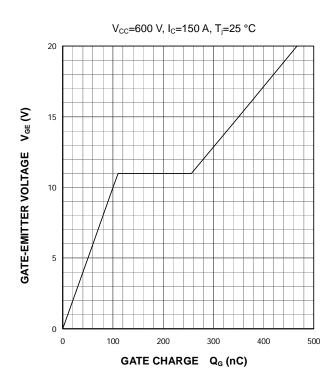


FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



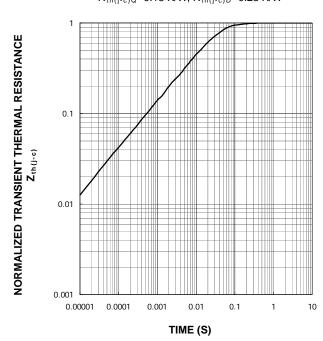


GATE CHARGE CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

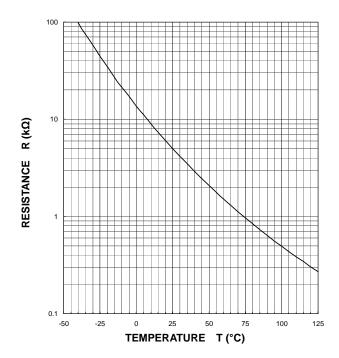
Single pulse, T_C=25 °C $R_{th(j-c)Q}$ =0.13 K/W, $R_{th(j-c)D}$ =0.23 K/W



PERFORMANCE CURVES

NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



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